

ISSUE CLASSIFICATION	
Class	Subclasses
1	1

FILED UNDER 85 U.S.C. 871

PATENT NUMBER

U.S. UTILITY Patent Application

RR O.I.P.E. SCANNED <u>A.C</u> Q.A. <u>RR</u>	PATENT DATE
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APPLICATION NO. 09/582067	CONT/PRIOR D F	CLASS 257	SUBCLASS 531	ART UNIT 2811 28.6	EXAMINER Miller Ford
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SEMICONDUCTOR SUBSTRATE WITH EMBEDDED ISOLATING LAYER FOR
INTEGRATED CIRCUITS

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date) _____ _____ _____ (Primary Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
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			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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